

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	61876	discharge adj2 lamp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 11:17
L2	15	inverter and (switch\$3 or transistor) and load adj2 circuit and sett\$3 same match\$3 with frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 11:34
L3	1	I1 and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 11:20
L4	142	inverter and (switch\$3 or transistor) and load adj2 circuit and sett\$3 same match\$3 and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 11:21
L5	2	I1 and I4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 11:21
L6	3	current adj2 limit\$3 adj3 device same (switch\$3 or transistor) and I1 and invert\$3 and sett\$3 and phase and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 11:37
L7	1	(current adj2 limit\$3 adj3 device same (switch\$3 or transistor) and I1 and invert\$3 and sett\$3 and phase and frequency).clm.	US-PGPUB	OR	ON	2005/12/20 12:39
L8	2526	315/209r.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 12:40
L9	1279	315/dig.7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/20 12:40